Refine Search

Search Results -

| Terms | Documents |
|---|-----------|
| L2 and (top adj electrode) and (bottom adj electrode) and (bus adj lines) | 3 |

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
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Search:

Database:

| L8 | | | |
|----|---------------|-------|---------------|
| | | | Refine Search |
| | Recall Text 👄 | Clear | Interrupt |

Search History

DATE: Saturday, September 11, 2004 Printable Copy Create Case

| Set Name side by side | Query | <u>Hit</u> Count | <u>Set</u> <u>Name</u> result set |
|--------------------------------|--|---------------------|-----------------------------------|
| DB=P | PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR | | |
| <u>L8</u> | L2 and (top adj electrode) and (bottom adj electrode) and (bus adj lines) | 3 | <u>L8</u> |
| <u>L7</u> | L6 and images | 5 | <u>L7</u> |
| <u>L6</u> | L5 and (spacers or barriers) | 5 | <u>L6</u> |
| <u>L5</u> | L4 and substrate | 11 | <u>L5</u> |
| <u>L4</u> | display and vacuum and (data adj lines) and (scan\$4 adj lines) and (ground adj lines) | 14 | <u>L4</u> |
| <u>L3</u> | L2 and vacuum and (data adj lines) and (scan\$4 adj lines) and (ground adj lines) | 1 | <u>L3</u> |
| <u>L2</u> | L1 and anode and cathode and substrate | 44 | <u>L2</u> |
| <u>L1</u> | (hot adj electron) and (cold adj cathode) and (flat adj panel adj display) and spacers and layer | 49 | <u>L1</u> |

END OF SEARCH HISTORY

Refine Search

Search Results -

| Terms Documen | |
|------------------|----|
| L5 and substrate | 10 |

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Search:

Database:

| L7 | | <u> </u> | Refine Search |
|----|---------------|----------|---------------|
| | Recall Text 👄 | Clear | Interrupt |

Search History

DATE: Saturday, September 11, 2004 Printable Copy Create Case

| Set Name side by side | Query | Hit Count | Set Name result set |
|--------------------------------|--|--------------|---------------------------|
| DB=F | PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR | | |
| <u>L7</u> | L5 and substrate | 10 | <u>L7</u> |
| <u>L6</u> | L5 and (cathode adj substrate) | 2 | <u>L6</u> |
| <u>L5</u> | L4 and layer | 10 | <u>L5</u> |
| <u>L4</u> | L3 and (electron adj sources) | 10 | <u>L4</u> |
| <u>L3</u> | L1 and ((electrical adj potential) or ground) | 17 | <u>L3</u> |
| <u>L2</u> | L1 and (ground adj lines) | 2 | <u>L2</u> |
| <u>L1</u> | (flat adj panel adj display) and (cold adj cathode) and (scan\$5 adj lines) and (data adj lines) and (spacers or barriers) | 19 | <u>L1</u> |

END OF SEARCH HISTORY